HALOGEN FREE



Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.52 \text{ V}$ at $I_F = 10 \text{ A}$



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 30 A			
V_{RRM}	150 V			
I _{FSM}	260 A			
V _F at I _F = 30 A	0.65 V			
T _J max.	175 °C			
Package	TO-3PW			
Diode variation	Dual common cathode			

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses

High efficiency operation

• Solder dip 275 °C max. 10 s, per JESD 22-B106

 Material categorization: For definitions of compliance please see www.vishav.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-3PW

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL V60H150PW		UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	150	V	
Maximum average forward rectified current (fig. 1)	per device		60	^	
	per diode	IF(AV)	30	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	260	А	
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +175	°C	



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 10 A	T _A = 25 °C	V _F ⁽¹⁾	0.66	-	V	
	I _F = 15 A			0.72	-		
	I _F = 30 A			0.80	0.93		
	I _F = 10 A	T _A = 125 °C		0.52	-		
	I _F = 15 A			0.56	-		
	I _F = 30 A			0.65	0.73		
Reverse current per diode	V _R = 120 V	T _A = 25 °C	I _R ⁽²⁾	1.7	-	μA	
		T _A = 125 °C		2.6	-	mA	
	V _R = 150 V	T _A = 25 °C		-	250	μA	
		T _A = 125 °C		3.7	36	mA	

Notes

⁽²⁾ Pulse test: Pulse width ≤ 20 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER		SYMBOL	V60H150PW	UNIT
Typical thermal resistance	per diode	$R_{ hetaJC}$	0.9	°C/W
	per device		0.6	- C/VV

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-3PW	V60H150PW-M3/4W	4.5	4W	30/tube	Tube		

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

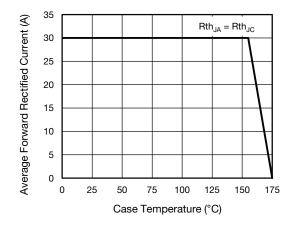


Fig. 1 - Maximum Forward Current Derating Curve

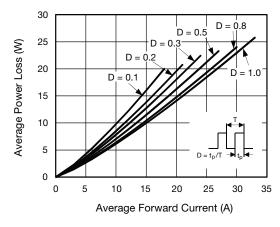


Fig. 2 - Forward Power Loss Characteristics Per Diode

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle



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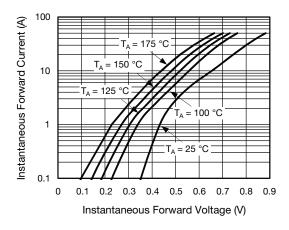


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

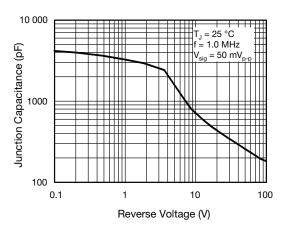


Fig. 5 - Typical Junction Capacitance Per Diode

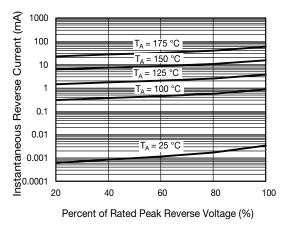


Fig. 4 - Typical Reverse Characteristics Per Diode

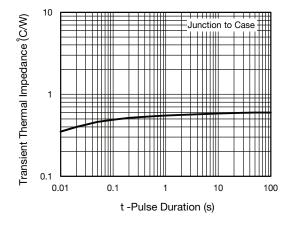
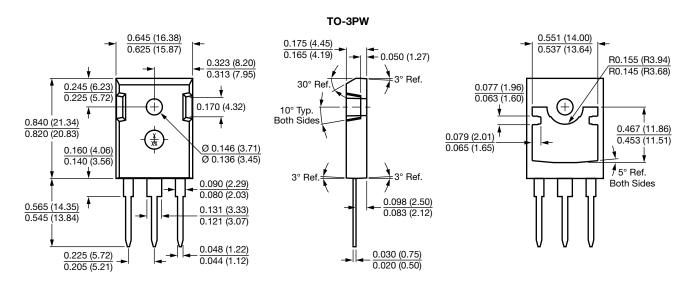


Fig. 6 - Typical Transient Thermal Impedance Per Device

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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